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** CONTINUING DATA *****

This application is a DIV of 09/963,382 09/27/2001 PAT 6,638,860 *WLL*

** FOREIGN APPLICATIONS *****

JAPAN 2000-294176 09/27/2000

JAPAN 2000-330968 10/30/2000 *WLL*

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35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged	Examiner's Signature <i>WLL</i>	Initials		

ADDRESS

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TITLE

Method and apparatus for processing substrates and method for manufacturing a semiconductor device

☐ All Fees